**Semnale de control MIPS32**

Semnale de Branch opționale: ? ϵ {gez, ne, gtz}, se va înlocui ? cu o valoare din paranteză, dacă e cazul

Tipuri de operații care se pun în paranteză la ALUOp si ALUCtrl:

(+), (-), (&), (|), (^), (<<*l*), (<<*lv*), (>>*l*), (>>*a*), (<)

Semnificații: & - AND, | - OR, ^ *- XOR, l* *- logic, a - aritmetic, v - cu variabilă*

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| **Instrucțiune** | **Opcode**  *Instr[31-26]* | **RegDst** | **ExtOp** | **ALUSrc** | **Branch** | **Br\_Gtz** | **Jump** | **MemWrite** | **MemtoReg** | **Reg Write** | **ALUOp[2:0]** | **function**  *Instr[5-0]* | **ALUCtrl[3:0]** |
| ADD | 000000 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 000 (R) | 100000 | 0000 (+) |
| SUB | 000000 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 000 (R) | 100010 | 0001 (-) |
| SLL | 000000 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 000 (R) | 000000 | 0010 (<<l) |
| SRL | 000000 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 000 (R) | 000010 | 0011 (>>l) |
| AND | 000000 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 000 (R) | 100100 | 0100 (&) |
| OR | 000000 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 000 (R) | 100101 | 0101 (|) |
| SLLV | 000000 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 000 (R) | 000100 | 0110 (<<lv) |
| SLT | 000000 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 000 (R) | 101010 | 0111 (<) |
| LW | 100011 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 001 (+) | X | 0000 (+) |
| SW | 101011 | X | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 001 (+) | X | 0000 (+) |
| ADDI | 001000 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 001 (+) | X | 0000 (+) |
| BEQ | 000100 | X | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 010 (-) | X | 0001 (-) |
| SLTI | 001010 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 011 (<) | X | 0111 (<) |
| BGTZ | 000111 | X | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 010 (-) | X | 0001 (-) |
| ANDI | 001100 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 100 (&) | X | 0100 (&) |
| J | 000010 | X | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | X | X | X |
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URL: <https://drive.google.com/file/d/1SI7x2Gp_2m3SEkwnXuGt4ns4voYzpGBH/view?usp=sharing>